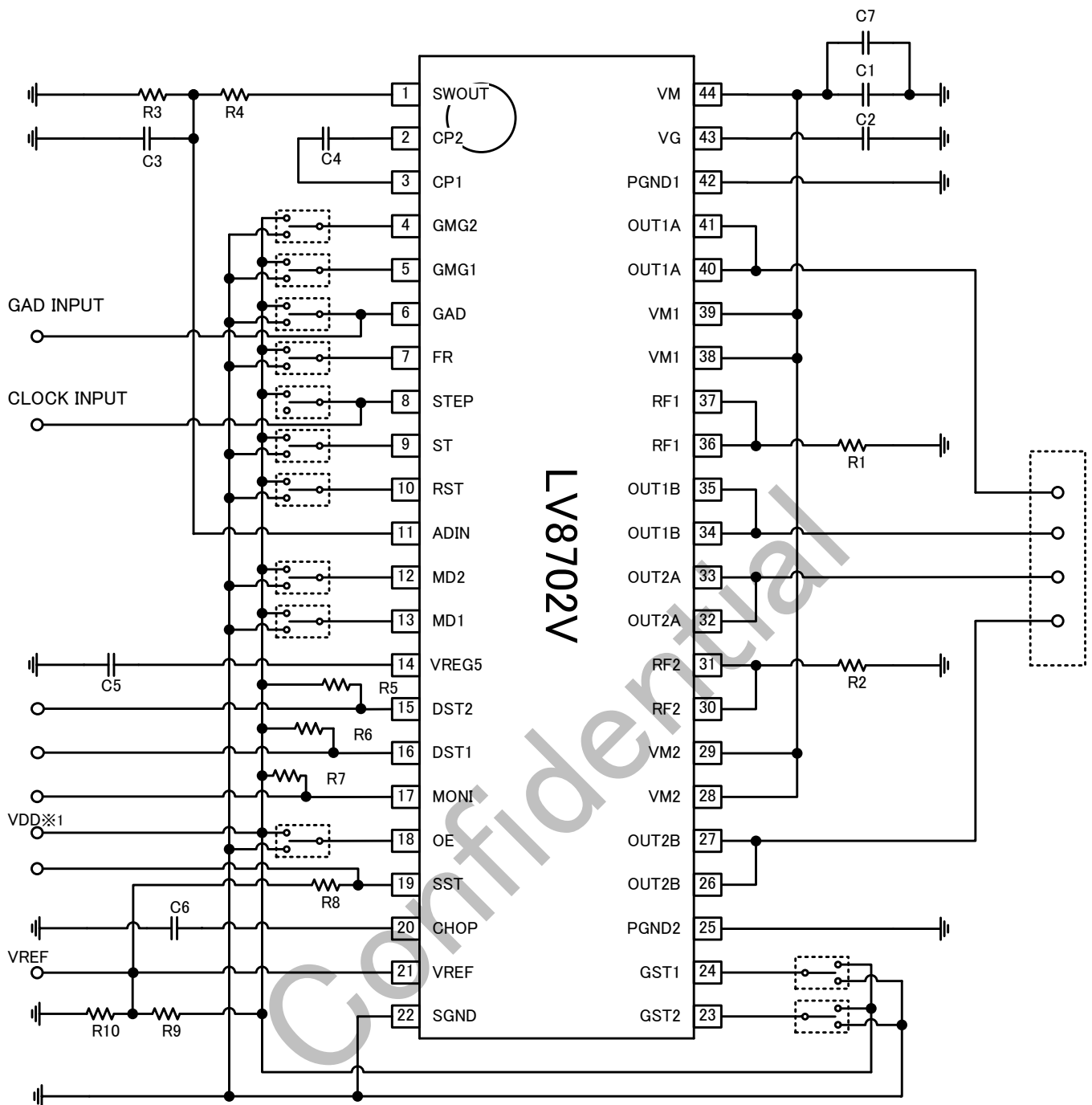




Resistor

R1	0.22Ω
R2	0.22Ω
R3	15kΩ
R4	100kΩ
R5	47kΩ
R6	47kΩ
R7	47kΩ
R8	—
R9	—
R10	—

Capacitor

C1	10uF/50V
C2	0.1uF/50V
C3	1000pF/50V
C4	0.1uF/50V
C5	0.1uF/50V
C6	150pF/50V
C7	—

*1 VDD is a power supply pin for SW/Nch open-drain. By supplying 3.3V or 5V, logic input setting is enabled.
VDD is also the pull-up power supply for Nch open-drain.